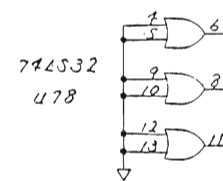
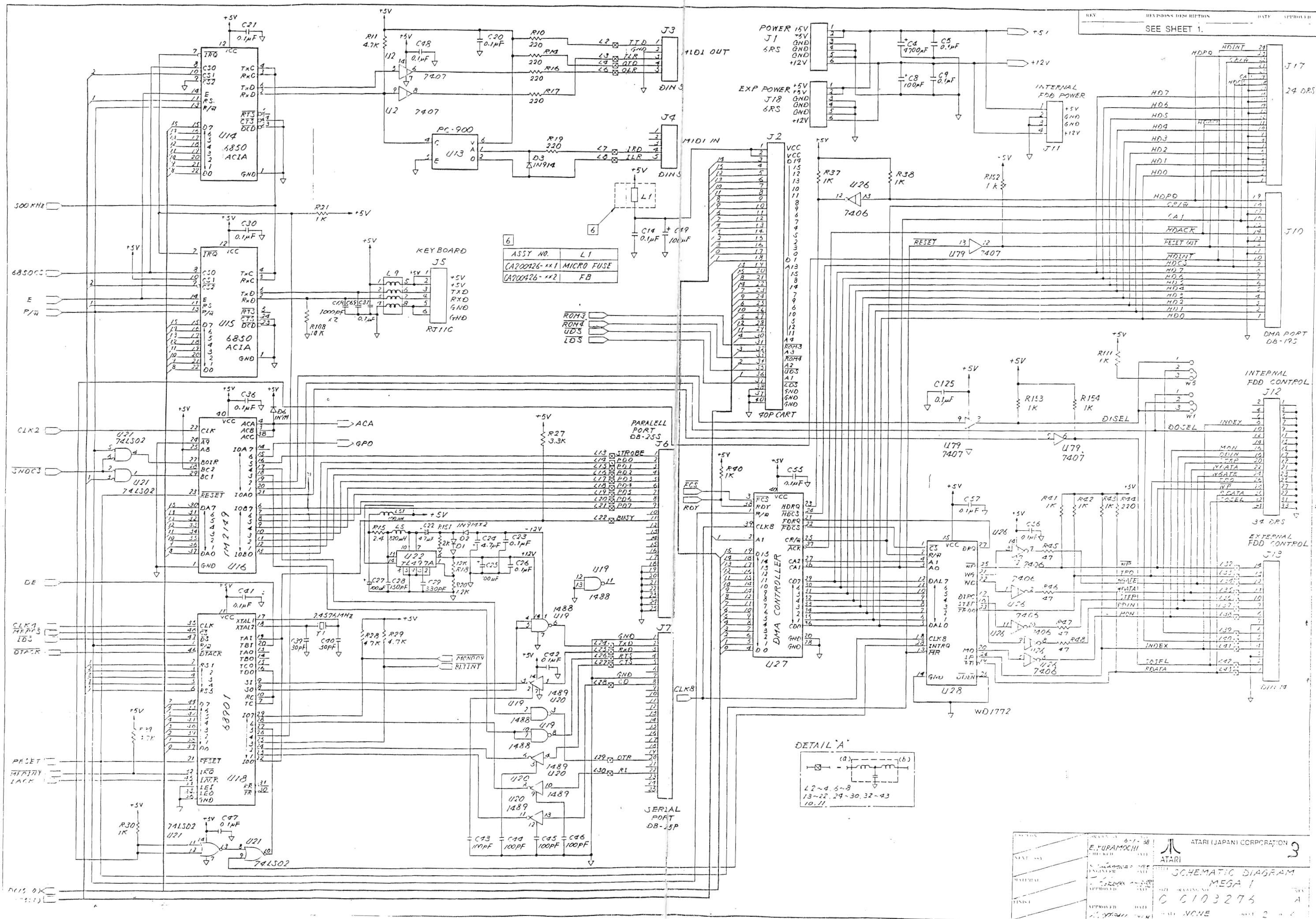
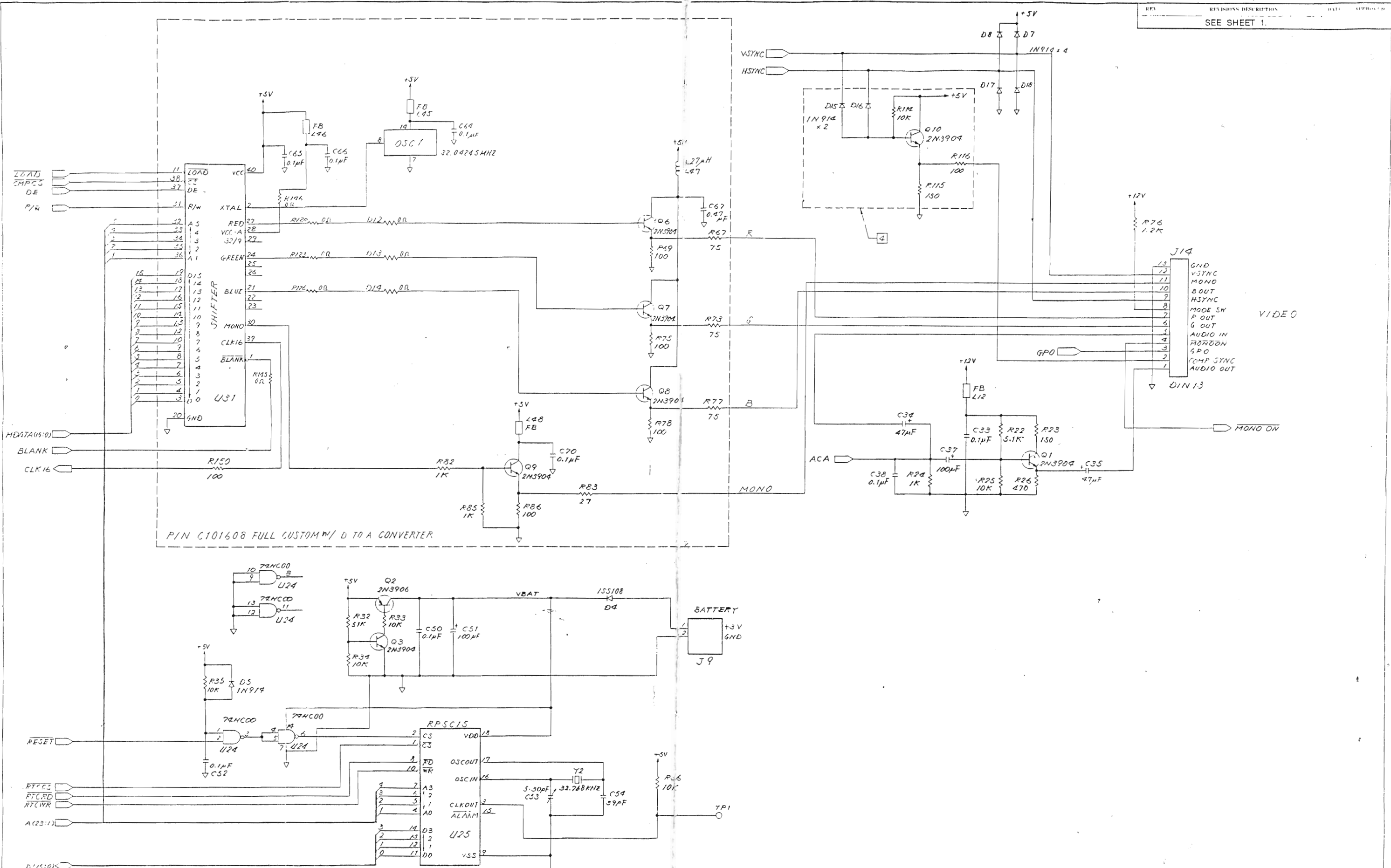


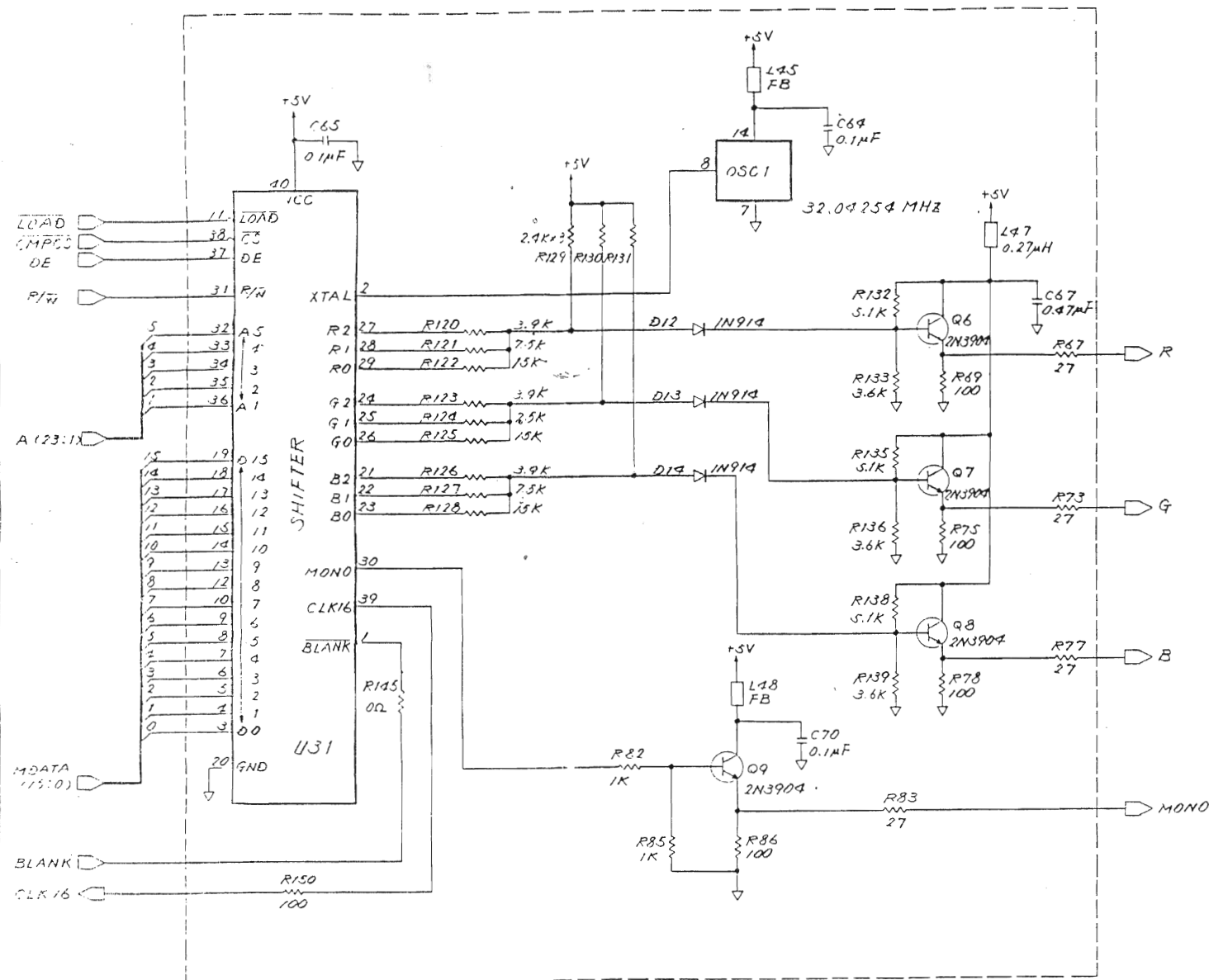
1. ROM TABLE: SEE SHEET 2 OF 6.
 2. NOT REQUIRED FOR ASSY NO. CA200426-XX1.
 3. PUT JUMPER WIRE FROM (A) TO (B) (IN DETAIL A L2~4, 5~8, 13~22, 24~30, 32~43)
ASSY NO. CA200426-XX2
 4. NOISE FILTER: SEE DETAIL 1.
 5. RESISTORS ARE MEASURED IN OHMS.
- NOTES: UNLESS OTHERWISE SPECIFIED:



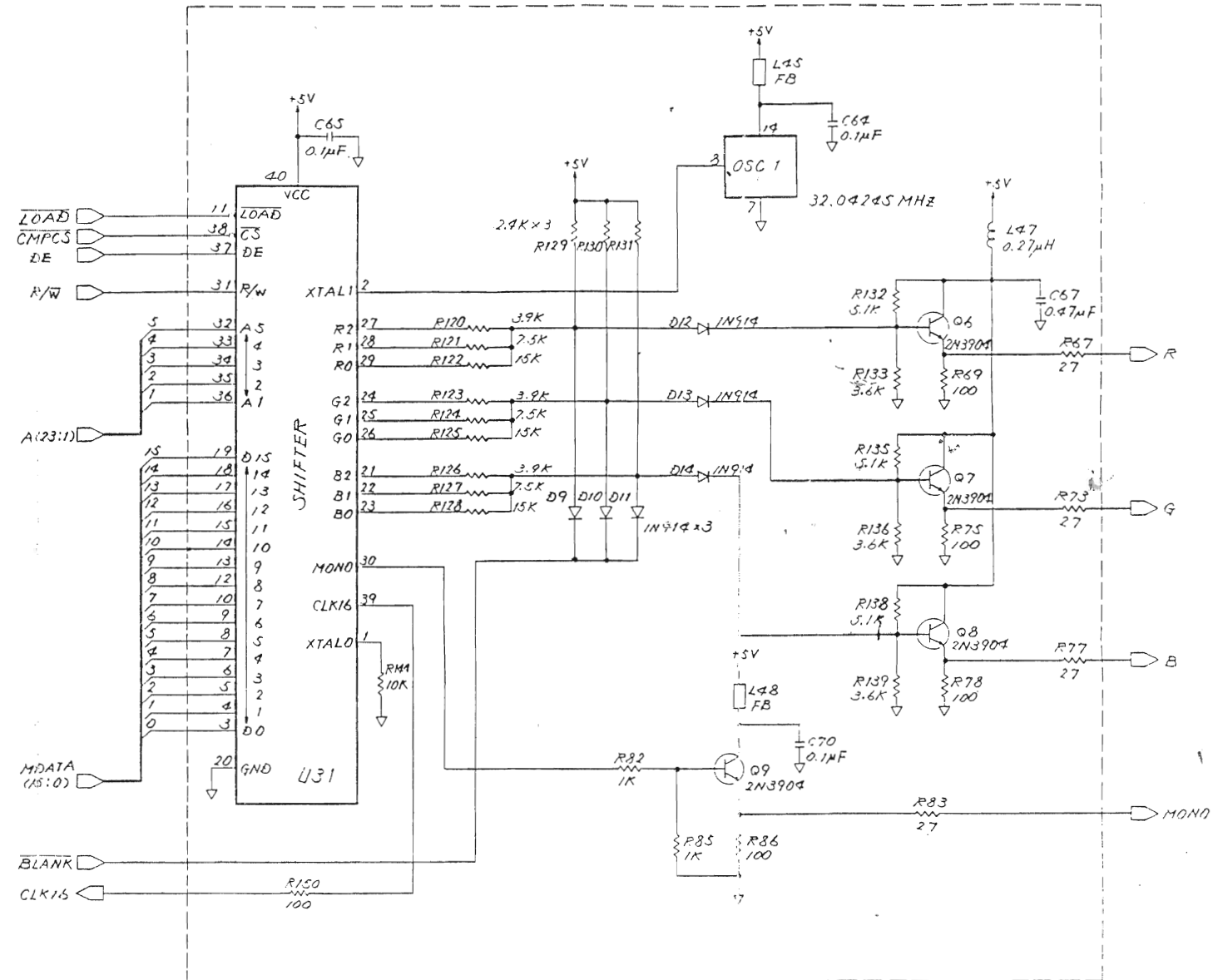
USED ON MEGA 1 NEXT ASSY CA200426-XXX MATERIAL FINISH	DRAWN BY E. KURAMOTO DATE 6-1-88 CHECKED DATE 6-13-88 APPROVED DATE 6-13-88	ATARI (JAPAN) CORPORATION ATARI SCHEMATIC DIAGRAM MEGA 1 C 103276 SCALE NONE SHEET 1 OF 6
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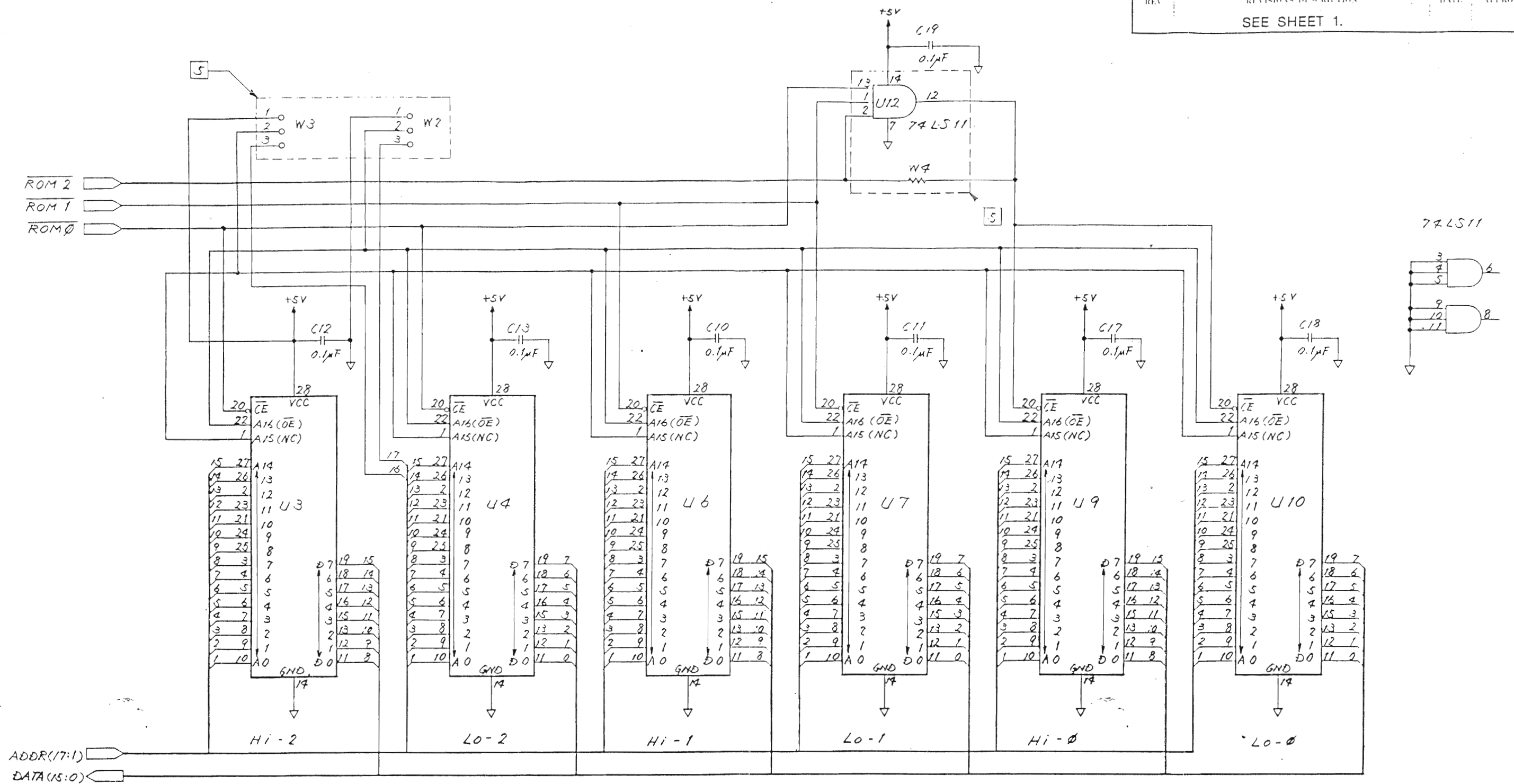




PIN 0070713 FULL CUSTOM V/D D TO A CONVERTER



PIN 0025914 GATE ARRAY V/D D TO A CONVERTER



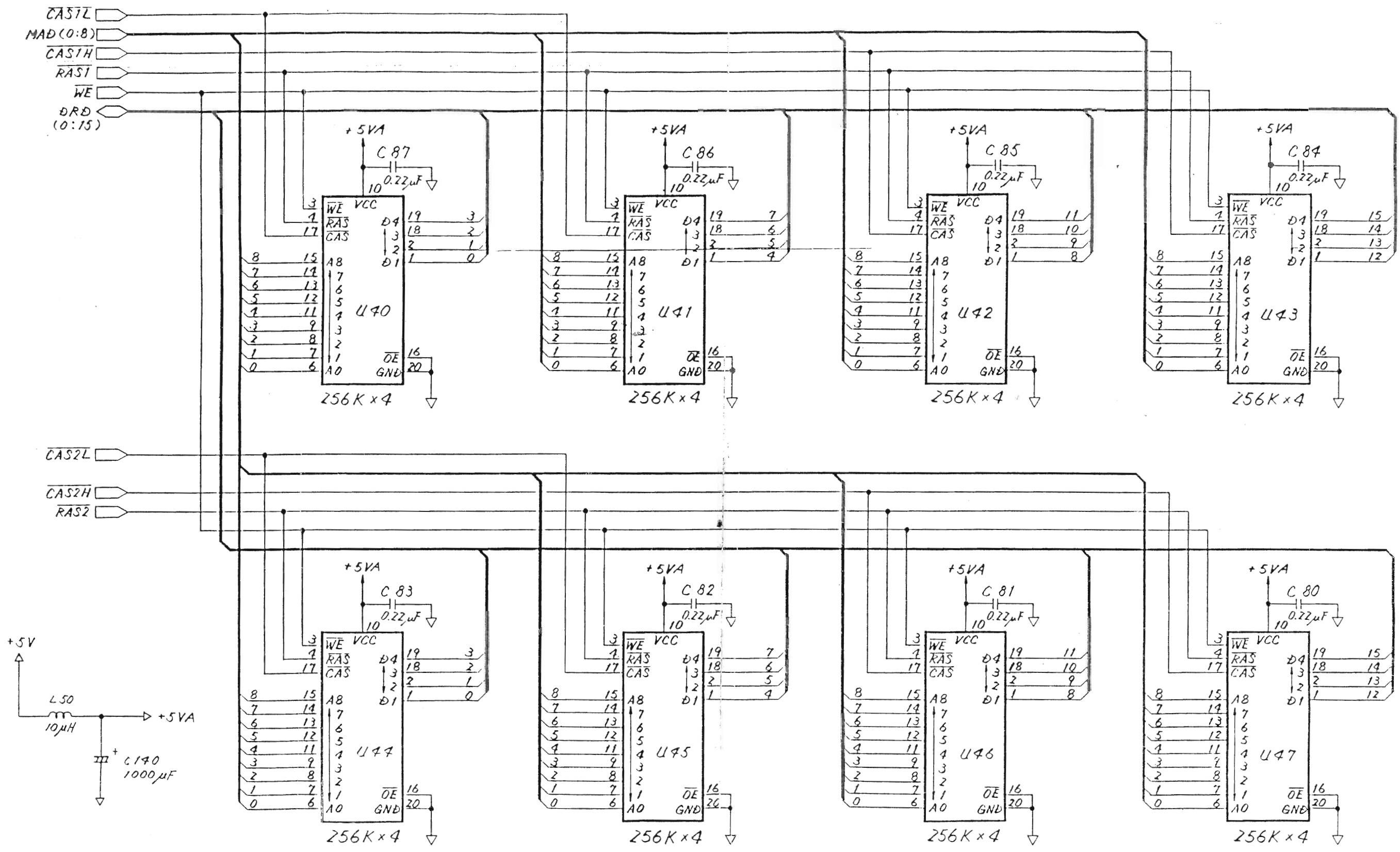
5 ROM TABLE

IC ; U12 (74LS11)
JUMPER ; W2, W3, W4

ROM	LOCATION			
	W2	W3	W4	U12
256K	1-2	1-2	JUMPER	NO USE
1M x 2	2-3	2-3	NON	USE
1M x 6	2-3	2-3	JUMPER	NO USE

NOTES - UNLESS OTHERWISE SPECIFIED :

USED ON	DRAWN BY N. J. [Signature]	DATE 6/1/80	ATARI (JAPAN) CORPORATION 6
SEPT ASSY	CHECKED K. K. [Signature]	DATE 6/1/80	
MATERIAL	ENGINEER K. K. [Signature]	DATE 6/1/80	TITLE SCHEMATIC DIAGRAM MEGA 1
FINISH	APPROVED N. J. [Signature]	DATE 6/1/80	SIZE C 103276
			REV A
			SCALE NONE
			SHEET 5 OF 6

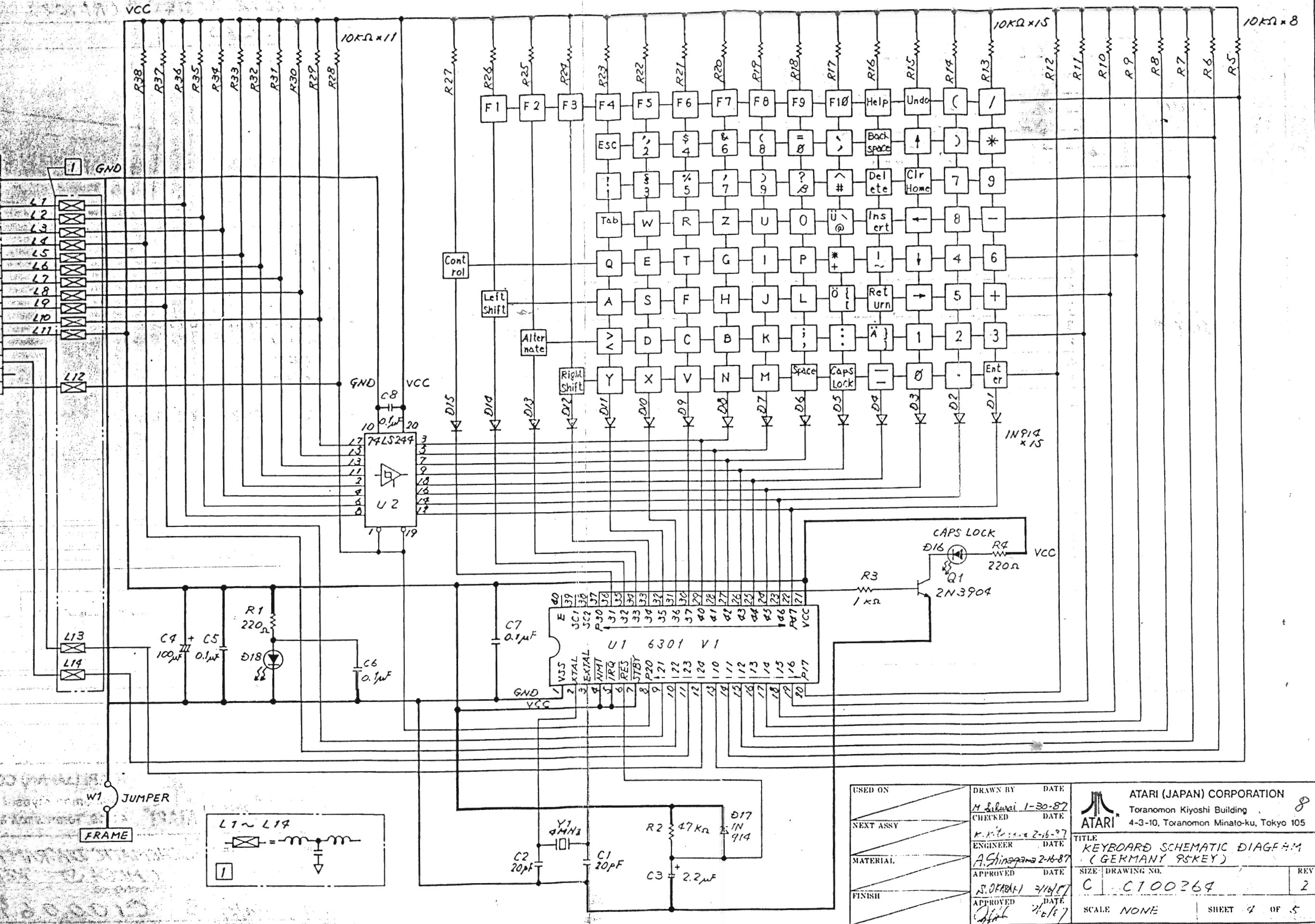


NOTES—UNLESS OTHERWISE SPECIFIED:

USED ON	DRAWN BY E. KURAMOCHI 6-1-88	DATE 6-1-88	ATARI (JAPAN) CORPORATION ATARI
TEST ASSY	CHECKED K. Kikuchi 6-1-88	DATE 6-1-88	1
MATERIAL	ENGINEER T. Kikuchi 6-13-88	DATE 6-13-88	SCHEMATIC DIAGRAM MEGA 1
FINISH	APPROVED S. Oikawa 6-13-88	DATE 6-13-88	SIZE: DRAWING NO. C C103276 SCALE: NONE SHEET 6 OF 6

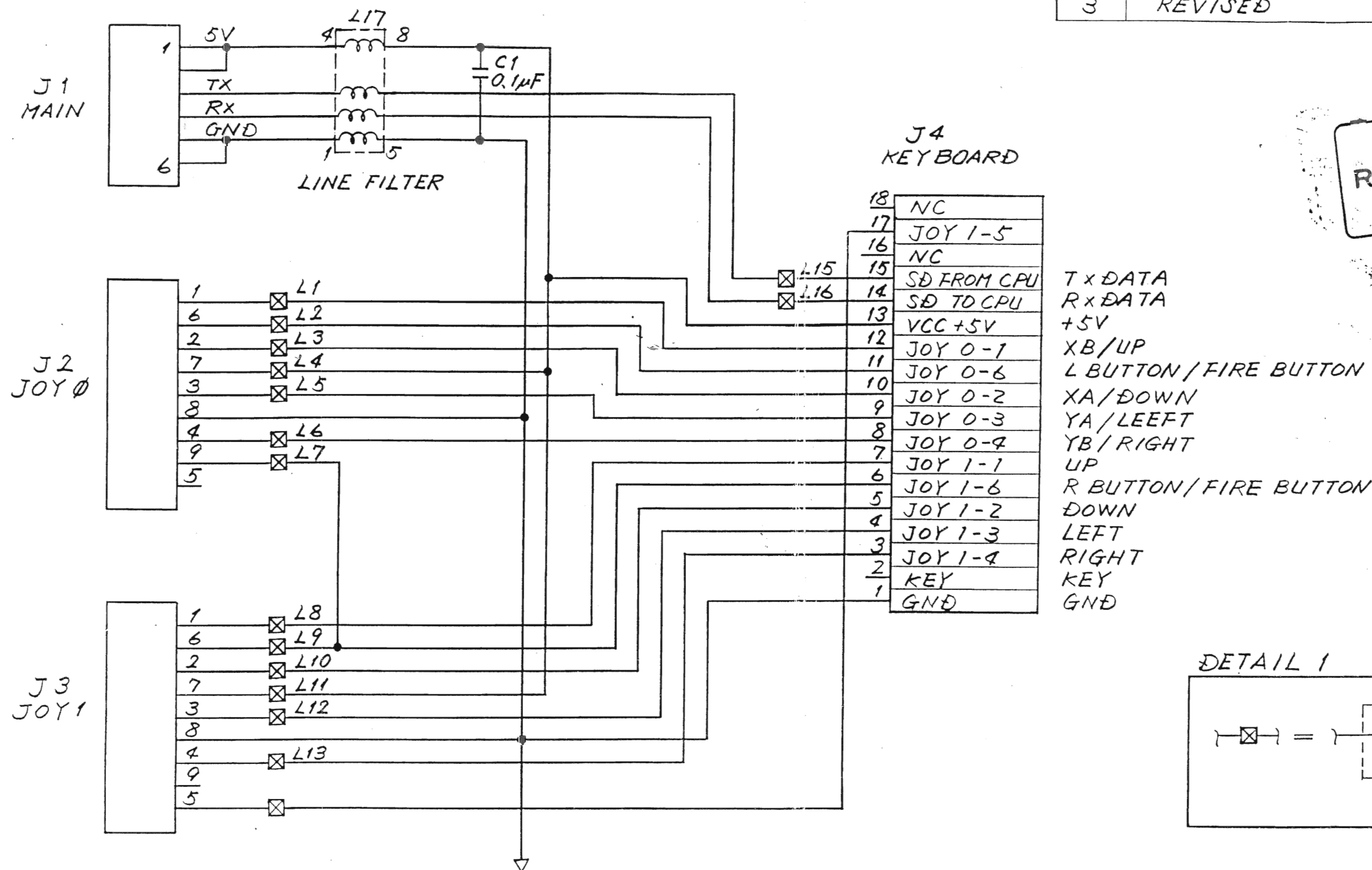
CONNECTOR

NO.	SIGNAL
1	GND
2	KEY
3	JOY1-4
4	JOY1-3
5	JOY1-2
6	JOY1-6
7	JOY1-1
8	JOY0-4
9	JOY0-3
10	JOY0-2
11	JOY0-6
12	JOY0-1
13	VCC+5V
14	SD TO CPU
15	SD FROM CPU
16	NC
17	JOY1-5



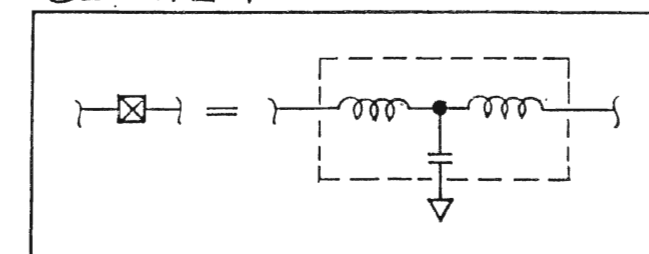
USED ON	DRAWN BY M. Sawai 1-30-87	DATE 1-30-87	<p>ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 4-3-10, Toranomon Minato-ku, Tokyo 105</p> <p>TITLE KEYBOARD SCHEMATIC DIAGRAM (GERMANY PSKEY)</p> <p>SIZE DRAWING NO. REV C C100264 2</p> <p>SCALE NONE SHEET 4 OF 5</p>
NEXT ASSY	CHECKED K. K. 2-16-87	DATE 2-16-87	
MATERIAL	ENGINEER A. Shindani 2-16-87	DATE 2-16-87	
FINISH	APPROVED S. OFABAKI 2-16-87	DATE 2-16-87	
	APPROVED 2-16-87	DATE 2-16-87	

REV	REVISIONS DESCRIPTION	DATE	APPROVED
1	PRELIMINARY RELEASE	7/19/87	JHLL
2	DELETE RES. & CAP. (R1, C2)	1-9-87	
3	REVISED	2/19/87	JHLL



REFERENCE ONLY

DETAIL 1

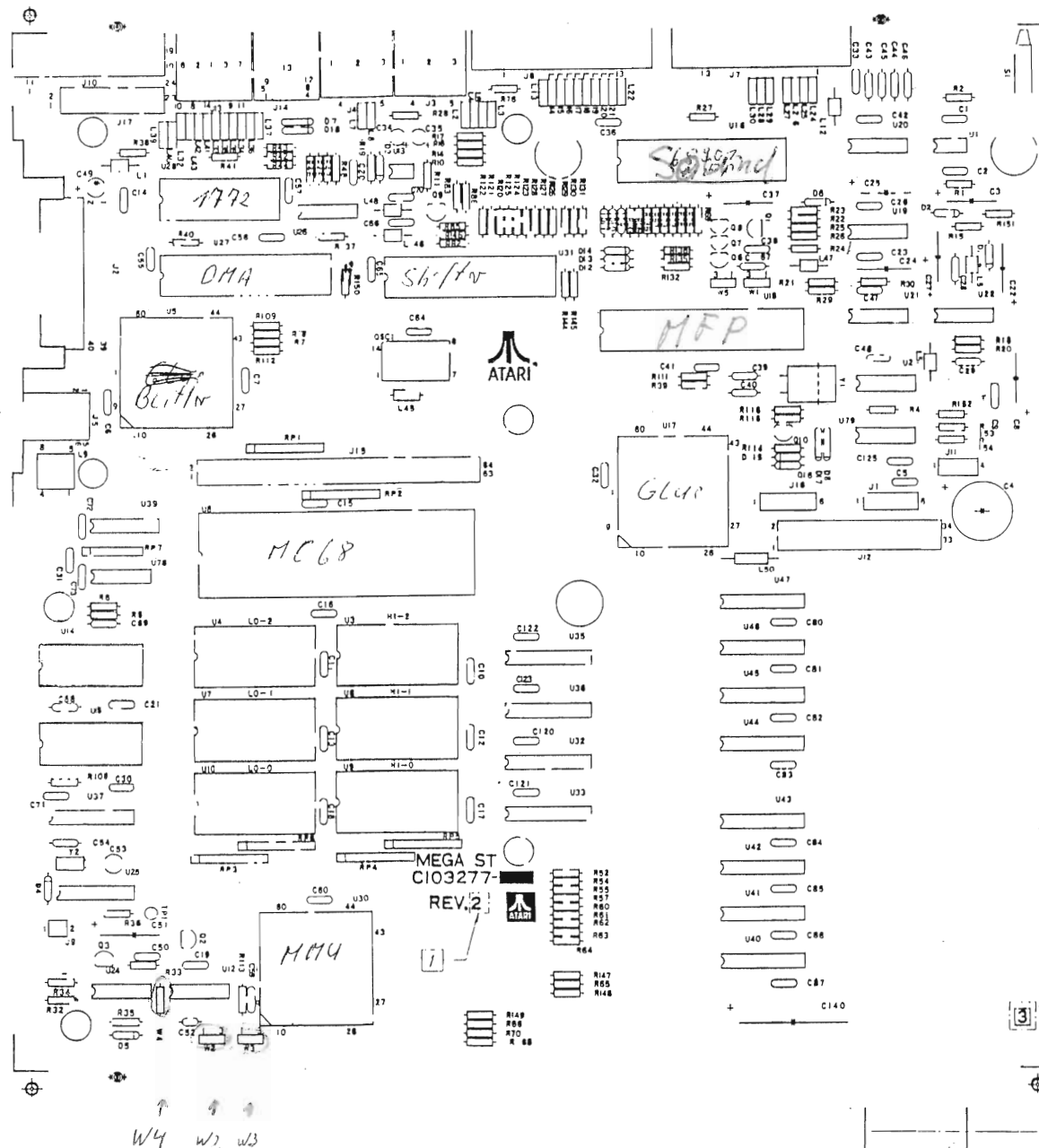


2. USE SUPPLIED P.C.B. C100063, REV. 1

1. NOISE FILTER: SEE DETAIL 1.

NOTES—UNLESS OTHERWISE SPECIFIED:

TOLERANCES		DRAWN BY	DATE	ATARI (JAPAN) CORPORATION	
UNDER 30	±0.1	I. Yashida	10-24-86	Toranomon Kiyoshi Building 3F	
30 THRU 300	±0.2	CHECKED	DATE	4-3-10, Toranomon Minato-ku, Tokyo 105	
OVER 300	±0.4	K. Kitagawa	10-27-86	TITLE	
MATERIAL		ENGINEER	DATE	SCHEMATIC DIAGRAM FOR SUB P.C.B.	
FINISH		A. Shinagawa	2-16-87	(MEGA ST KEYBOARD)	
NEXT ASSY		APPROVED	DATE	SIZE	DRAWING NO.
APPLICATION		S. Okazaki	2/18/87	B	C100069
		APPROVED	DATE	SCALE	REV
		JHLL	7/19/87	NONE	3
				SHEET	1 OF 1



NOTES—UNLESS OTHERWISE SPECIFIED:

TOLERANCES		DRAWN BY		DATE		ATARI JAPAN CORPORATION	
UNDER 30	±0.1	A. Tamba		10/14/88		ATARI	
30 THRU 300	±0.2	CHECKED		DATE		TITLE	
OVER 300	±0.4	ENGINEER		DATE		PCB MEGA 1	
MATERIAL		APPROVED		DATE		SIZE	
FINISH		APPROVED		DATE		DRAWING NO.	
NEXT ASSY		APPROVED		DATE		C 0103277	
USED ON		APPROVED		DATE		REV	
APPLICATION		APPROVED		DATE		A	
		SCALE		N/A		SHEET 2 OF 6	